

**DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)**

Time table for:

**B. Sc. Hons Electronics V - Sem**

**w.e.f Sept 15, 2021**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>	DSP E-2 115	DSP E-2 115	Semi. Fab. NT 104	Semi. Fab. NT 104	<b>LUNCH BREAK</b>				
<b>Tuesday</b>	EMT Lab (RK,NT,E-3) Lab. 116 and 117					DSP Lab (AM,E-2,E-3) Lab. 116 and 117			
<b>Wednesday</b>	Semi. Fab. NT 115	Semi. Fab. NT 115	Micro.Proc. RK 104	Micro.Proc. RK 104		EMT E-3 115	EMT E-3 115		
<b>Thursday</b>	EMT E-3 115	EMT E-3 115	Micro.Proc. RK 115	Micro.Proc. RK 115		DSP E-2 115	DSP E-2 115		
<b>Friday</b>	Semi. Fab. Lab (NT,RK,E-3) Lab. 116 and 117					Microprocessor. Lab (RK,AM, E-2) Lab. 116 and 117			
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

No Change in the Time Table is possible without the permission of the Principal

**Abbr. Title of the paper**

Micro.Proc.	MICROPROCESSORS AND MICROCONTROLLERS
EMT	ELECTROMAGNETICS
Semi. Fab.	SEMICONDUCTOR FABRICATION AND CHARACTERIZATION
DSP	DIGITAL SIGNAL PROCESSING

**DU Paper Code**

32511501
32511503
32517902
32517907

RK	Dr. Ravinder Kaur
PKS	Dr. Poonam Kasturi
NT	Dr. Neeraj Tyagi
AM	Dr. Anurag Mishra
MS	Dr. Manoj Saxena
E-1	Neha
E-2	Naveen
E-3	Ajit

Convener - Time Table

Principal