

DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

B. Sc. Hons Electronics I - Sem

Effective From: July 20, 2018

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
Monday	Generic 104	Generic 104	Net. Anal. 104 E-2	Net. Anal. 104 E-2	LUNCH BREAK				
Tuesday	HINDI 104	HINDI 104	Math. Fnd 104 E-1	Math. Fnd 104 E-1		Network Analysis Lab. NT, AM, RK, E-2, E-3 Electronics Lab. 116 and 117			
Wednesday	Math. Fnd 104 E-1	Math. Fnd 104 E-1	AECC 104	AECC 104		Generic Lab/Tutorial			
Thursday	Mathematical Foundation Lab. MS, RK, E-1 Electronics Lab. 116 and 117					Generic 104	Generic 104		
Friday			AECC 104	AECC 104		Net. Anal. 104 E-3	Net. Anal. 104 E-3	HINDI 104	HINDI 104
Saturday									

Classes begin at 9:00 am on all days.

No Change in the Time Table is possible without the permission of the Principal

RK	Dr. Ravinder Kaur	E-1	To be Appointed
PKS	Dr. Poonam Kasturi	E-2	To be Appointed
NT	Dr. Neeraj Tyagi	E-3	To be Appointed
AM	Dr. Anurag Mishra		
MS	Dr. Manoj Saxena		

Convener
Time Table

Principal

DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for: **B. Sc. Hons Electronics III - Sem**

Effective From: July 20, 2018

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm		5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
Monday	Digital Electronics and Verilog Lab. PKS, MS, E-1 Electronics Lab. 116 and 117				LUNCH BREAK	SEC 115 E-1	SEC 115 E-1		
Tuesday	Electronic Cir. 115 MS	Electronic Cir. 115 MS	Digital Elec. 115 PKS	Digital Elec. 115 PKS		Generic Lab./Tutorial			
Wednesday	Generic 115	Generic 115	C Prog. 115 AM	C Prog. 115 AM		Electronic Circuits Lab. MS, PKS, E-1 Electronics Lab. 116 and 117			
Thursday	Electronic Cir. 115 MS	Electronic Cir. 115 MS	Digital Elec. 115 PKS	Digital Elec. 115 PKS		C Programming and Data Structures Lab. AM,PKS, E-3 Electronics Lab. 116 and 117			
Friday	Skill Enhancement Course Lab. MS, PKS, E-1 Electronics Lab. 116 and 117					Generic 115	Generic 115	C Prog. 115 AM	C Prog. 115 AM
Saturday									

Classes begin at 9:00 am on all days.

No Change in the Time Table is possible without the permission of the Principal

RK	Dr. Ravinder Kaur	E-1	To be Appointed
PKS	Dr. Poonam Kasturi	E-2	To be Appointed
NT	Dr. Neeraj Tyagi	E-3	To be Appointed
AM	Dr. Anurag Mishra		
MS	Dr. Manoj Saxena		

Convener
Time Table

Principal

DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

B. Sc. Hons Electronics V - Sem

Effective From: July 20, 2018

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
Monday	Semi. Fab. 115 NT	Semi. Fab. 115 NT	Microproc. 115 RK	Microproc. 115 RK	LUNCH BREAK	Microprocessors and Microcontroller Lab. RK, AM, E-2 Electronics Lab. 116 and 117			
Tuesday	DSP Lab. RK, E-2, E-3 Electronics Lab. 116 and 117					EMT 115 E-3	EMT 115 E-3		
Wednesday	Electromagnetics Lab. NT, RK, E-3 Electronics Lab. 116 and 117					DSP 115 E-2	DSP 115 E-2		
Thursday	DSP 104 E-2	DSP 104 E-2	Semi. Fab. 104 NT	Semi. Fab. 104 NT					
Friday	EMT 115 E-3	EMT 115 E-3	Microproc. 115 RK	Microproc. 115 RK		Semiconductor Fabrication and Charecterization Lab. NT, AM, E-2 Electronics Lab. 116 and 117			
Saturday									

Classes begin at 9:00 am on all days.

No Change in the Time Table is possible without the permission of the Principal

RK	Dr. Ravinder Kaur	E-1	To be Appointed
PKS	Dr. Poonam Kasturi	E-2	To be Appointed
NT	Dr. Neeraj Tyagi	E-3	To be Appointed
AM	Dr. Anurag Mishra		
MS	Dr. Manoj Saxena		

Convener
Time Table

Principal

DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for: **Dr. Ravinder Kaur** **Effective From: July 20, 2018**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm		5 1:30 pm-2:30 pm	6 2:30 pm-3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
Monday			Microproc. 115 EH-V	Microproc. 115 EH-V	LUNCH BREAK	Microprocessors and Microcontroller Lab. EH-V Electronics Lab. 116 and 117			
Tuesday						Network Analysis Lab. EH-I Electronics Lab. 116 and 117			
Wednesday			Electromagnetics Lab. EH-V Electron. Lab. 116 & 117						
Thursday	Mathematical Foundation Lab. EH-I Electronics Lab. 116 and 117								
Friday			Microproc. 115 EH-V	Microproc. 115 EH-V					
Saturday									

Classes begin at 9:00 am on all days.

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Convener Time Table

Principal

DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for: **Dr. Poonam Kasturi** **Effective From: July 20, 2018**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm		5 1:30 pm-2:30 pm	6 2:30 pm-3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
Monday	Digital Electronics and Verilog Lab. EH-III Electronics Lab. 116 and 117				LUNCH BREAK				
Tuesday			Digital Elec EH-III 115	Digital Elec EH-III 115					
Wednesday						Electronic Circuits Lab. EH-III Electronics Lab. 116 and 117			
Thursday			Digital Elec EH-III 115	Digital Elec EH-III 115		Advanced Data Structures Lab. EH-III Electronics Lab. 116 and 117			
Friday	SEC Lab. EH-III Electronic Lab. 116 and 117								
Saturday									

Classes begin at 9:00 am on all days.

No Change in the Time Table is possible without the permission of the Principal

Convener Time Table

Principal

DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for: **Dr. Anurag Mishra** Effective From: **July 20, 2018**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm		5 1:30 pm-2:30 pm	6 2:30 pm-3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
Monday					LUNCH BREAK	Microproc. and Microcont. Lab. EH-V Electronics Lab. 116 and 117			
Tuesday						Network Analysis Lab. EH-I Electronics Lab. 116 and 117			
Wednesday			C Prog. 115 EH-III	C Prog. 115 EH-III					
Thursday						Programming and Data Structures Lab. EH-III Electronics Lab. 116 and 117			
Friday						Semi. Fab. Lab. EH-V Electronics Lab. 116 and 117	C Prog. 115 EH-III	C Prog. 115 EH-III	
Saturday									

Classes begin at 9:00 am on all days.

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Convener Time Table

Principal

DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for: **Dr. Neeraj Tyagi** Effective From: **July 20, 2018**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm		5 1:30 pm-2:30 pm	6 2:30 pm-3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
Monday	Semi. Fab. 115 EH-V	Semi. Fab. 115 EH-V			LUNCH BREAK				
Tuesday	DSP Lab. EH-V Electronics Lab. 116 & 117					Network Analysis Lab. EH-I Electronics Lab. 116 and 117			
Wednesday	Electromagnetics Lab. EH-V Electronics Lab. 116 and 117								
Thursday			Semi. Fab. 104 EH-V	Semi. Fab. 104 EH-V					
Friday						Semiconductor Fabrication and Characterization EH-V Electronics Lab. 116 and 117			
Saturday									

Classes begin at 9:00 am on all days.

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Convener Time Table

Principal

DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for: **Dr. Manoj Saxena** **Effective From: July 20, 2018**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm		5 1:30 pm-2:30 pm	6 2:30 pm-3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
Monday	Digital Electronics and Verilog Lab. EH-III Electronics Lab. 116 and 117				LUNCH BREAK				
Tuesday	Electronic Cir. EH-III 115	Electronic Cir. EH-III 115							
Wednesday						Electronic Circuits Lab. EH-III Electronics Lab. 116 and 117			
Thursday	Electronic Cir. EH-III 115	Electronic Cir. EH-III 115	Mathematical Found. Lab. EH-I Electronics Lab. 116 and 117						
Friday	SEC Lab. EH-III Electronic Lab. 116 and 117								
Saturday									

Classes begin at 9:00 am on all days.

No Change in the Time Table is possible without the permission of the Principal

Convener Time Table

Principal

DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for: **E-1** **Effective From: July 20, 2018**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm		5 1:30 pm-2:30 pm	6 2:30 pm-3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
Monday	Digital Electronics and Verilog Lab. EH-III Electronics Lab. 116 and 117				LUNCH BREAK	SEC 115 EH-III	SEC 115 EH-III		
Tuesday			Math. Fnd 104 EH-I	Math. Fnd 104 EH-I					
Wednesday	Math. Fnd 104 EH-I	Math. Fnd 104 EH-I				Electronic Circuits Lab. EH-III Electronics Lab. 116 and 117			
Thursday	Mathematical Foundation Lab. EH-I Electronics Lab. 116 and 117								
Friday	Skill Enhancement Course Lab. EH-III Electronics Lab. 116 and 117								
Saturday									

Classes begin at 9:00 am on all days.

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Convener Time Table

Principal

DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

E-2

Effective From: July 20, 2018

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm		5 1:30 pm-2:30 pm	6 2:30 pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
Monday			Net. Anal. 104 EH-I	Net. Anal. 104 EH-I	LUNCH BREAK	Microprocessors and Microcontroller Lab. EH-V Electronics Lab. 116 and 117			
Tuesday	DSP Lab. EH-V Electronics Lab. 116 and 117					Network Analysis Lab. EH-I Electronics Lab. 116 and 117			
Wednesday						DSP 115 EH-V	DSP 115 EH-V		
Thursday	DSP 104 EH-V	DSP 104 EH-V							
Friday						Semiconductor Fabrication and Charecterization Lab. EH-V Electronics Lab. 116 and 117			
Saturday									

Classes begin at 9:00 am on all days.

No Change in the Time Table is possible without the permission of the Principal

Convener Time Table

Principal

DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

E-3

Effective From: July 20, 2018

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm		5 1:30 pm-2:30 pm	6 2:30 pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
Monday					LUNCH BREAK	Microprocessors and Microcontroller Lab. EH-V Electronics Lab. 116 and 117			
Tuesday			DSP Lab. EH-V Electron. Lab. 116 & 117			EMT 115 EH-V	EMT 115 EH-V	Network Analysis Lab. EH-I Electronics Lab. 116 and 117	
Wednesday	Electromagnetics Lab. EH-V Electronics Lab. 116 and 117								
Thursday						Programming and Data Structures Lab. EH-III Electronics Lab. 116 and 117			
Friday	EMT 115 EH-V	EMT 115 EH-V				Net. Anal. 104 EH-I	Net. Anal. 104 EH-I		
Saturday									

Classes begin at 9:00 am on all days.

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Convener Time Table

Principal