

Marks 567 : Bachelor of Science (Honours Course) Physics, 6, SEMESTER, 2022-2023-MAY:REGULAR, IA

Submit Marks

<input type="checkbox"/>	S.No.	Exam Roll Number	Enrolment Number	Student Name	Paper Code	Paper Name	Term	Maximum Marks	Marks Uploaded	Component Status	Marks Status
<input type="checkbox"/>	1	19015567006	19dduccbsh00007	AZOOB KHAN	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER			Not Entered	
<input type="checkbox"/>	2	19015567006	19dduccbsh00007	AZOOB KHAN	32221602	STATISTICAL MECHANICS	6 SEMESTER			Not Entered	
<input type="checkbox"/>	3	19015567020	19DDUCBSPH000026	POOJA BAI MEENA	32221602	STATISTICAL MECHANICS	6 SEMESTER			Not Entered	
<input type="checkbox"/>	4	19015567023	19DDUCBSPH000030	RAVI MEENA	32221602	STATISTICAL MECHANICS	6 SEMESTER			Not Entered	
<input type="checkbox"/>	5	19015567045	19DDUCBSPH000003	AJAY KUMAR	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER			Not Entered	
<input type="checkbox"/>	6	19015567045	19DDUCBSPH000003	AJAY KUMAR	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER			Not Entered	
<input type="checkbox"/>	7	19015567045	19DDUCBSPH000003	AJAY KUMAR	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER			Not Entered	
<input type="checkbox"/>	8	19015567045	19DDUCBSPH000003	AJAY KUMAR	32221602	STATISTICAL MECHANICS	6 SEMESTER			Not Entered	
<input type="checkbox"/>	9	20015567001	20DDUCBSPH000001	AASHISH PRASAD	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	14	Ok	DRAFT
<input type="checkbox"/>	10	20015567001	20DDUCBSPH000001	AASHISH PRASAD	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	14	Ok	DRAFT
<input type="checkbox"/>	11	20015567001	20DDUCBSPH000001	AASHISH PRASAD	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	2	Ok	DRAFT
<input type="checkbox"/>	12	20015567001	20DDUCBSPH000001	AASHISH PRASAD	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	5	Ok	DRAFT
<input type="checkbox"/>	13	20015567002	20DDUCBSPH000002	AAYUSHI JOSHI	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	7	Ok	DRAFT
<input type="checkbox"/>	14	20015567002	20DDUCBSPH000002	AAYUSHI JOSHI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	15	20015567002	20DDUCBSPH000002	AAYUSHI JOSHI	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	0	Ok	DRAFT
<input type="checkbox"/>	16	20015567002	20DDUCBSPH000002	AAYUSHI JOSHI	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	7	Ok	DRAFT
<input type="checkbox"/>	17	20015567003	20DDUCBSPH000003	ABHINAV ACHARYA	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	10	Ok	DRAFT
<input type="checkbox"/>	18	20015567003	20DDUCBSPH000003	ABHINAV ACHARYA	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	13	Ok	DRAFT
<input type="checkbox"/>	19	20015567003	20DDUCBSPH000003	ABHINAV ACHARYA	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	0	Ok	DRAFT
<input type="checkbox"/>	20	20015567003	20DDUCBSPH000003	ABHINAV ACHARYA	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	8	Ok	DRAFT
<input type="checkbox"/>	21	20015567005	20DDUCBSPH000005	ADITYA PRATAP SINGH	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	22	20015567005	20DDUCBSPH000005	ADITYA PRATAP SINGH	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	20	Ok	DRAFT
<input type="checkbox"/>				ADITYA		ELECTROMAGNETIC	6				

<input type="checkbox"/>	24	20015567005	20DDUCBSPH000005	ADITYA PRATAP SINGH	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	10	Ok	DRAFT
<input type="checkbox"/>	25	20015567006	20DDUCBSPH000006	AKHIL PRATAP SINGH	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	13	Ok	DRAFT
<input type="checkbox"/>	26	20015567006	20DDUCBSPH000006	AKHIL PRATAP SINGH	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	19	Ok	DRAFT
<input type="checkbox"/>	27	20015567006	20DDUCBSPH000006	AKHIL PRATAP SINGH	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	28	20015567006	20DDUCBSPH000006	AKHIL PRATAP SINGH	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	8	Ok	DRAFT
<input type="checkbox"/>	29	20015567007	20DDUCBSPH000007	AKSHAY KUMAR	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	14	Ok	DRAFT
<input type="checkbox"/>	30	20015567007	20DDUCBSPH000007	AKSHAY KUMAR	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	17	Ok	DRAFT
<input type="checkbox"/>	31	20015567007	20DDUCBSPH000007	AKSHAY KUMAR	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	32	20015567007	20DDUCBSPH000007	AKSHAY KUMAR	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	18	Ok	DRAFT
<input type="checkbox"/>	33	20015567008	20DDUCBSPH000008	AKSHIT GUPTA	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	34	20015567008	20DDUCBSPH000008	AKSHIT GUPTA	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	9	Ok	DRAFT
<input type="checkbox"/>	35	20015567008	20DDUCBSPH000008	AKSHIT GUPTA	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	10	Ok	DRAFT
<input type="checkbox"/>	36	20015567008	20DDUCBSPH000008	AKSHIT GUPTA	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	14	Ok	DRAFT
<input type="checkbox"/>	37	20015567009	20DDUCBSPH000009	AMAN VERMA	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	16	Ok	DRAFT
<input type="checkbox"/>	38	20015567009	20DDUCBSPH000009	AMAN VERMA	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	23	Ok	DRAFT
<input type="checkbox"/>	39	20015567009	20DDUCBSPH000009	AMAN VERMA	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	16	Ok	DRAFT
<input type="checkbox"/>	40	20015567009	20DDUCBSPH000009	AMAN VERMA	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	21	Ok	DRAFT
<input type="checkbox"/>	41	20015567010	20DDUCBSPH000010	AMBER MISHRA	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	10	Ok	DRAFT
<input type="checkbox"/>	42	20015567010	20DDUCBSPH000010	AMBER MISHRA	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	17	Ok	DRAFT
<input type="checkbox"/>	43	20015567010	20DDUCBSPH000010	AMBER MISHRA	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	44	20015567010	20DDUCBSPH000010	AMBER MISHRA	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	45	20015567011	20DDUCBSPH000011	AMIT KUMAR GOND	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	46	20015567011	20DDUCBSPH000011	AMIT KUMAR GOND	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	13	Ok	DRAFT
<input type="checkbox"/>	47	20015567011	20DDUCBSPH000011	AMIT KUMAR GOND	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	2	Ok	DRAFT
<input type="checkbox"/>	48	20015567011	20DDUCBSPH000011	AMIT KUMAR GOND	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	7	Ok	DRAFT
<input type="checkbox"/>	49	20015567012	207461/DDUC/53/4	ANIL GOSWAMI	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	13	Ok	DRAFT
<input type="checkbox"/>	50	20015567012	207461/DDUC/53/4	ANIL GOSWAMI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	8	Ok	DRAFT

<input type="checkbox"/>	51	20015567012	207461/DDUC/53/4	ANIL GOSWAMI	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	52	20015567012	207461/DDUC/53/4	ANIL GOSWAMI	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	13	Ok	DRAFT
<input type="checkbox"/>	53	20015567014	20DDUCBSPH000016	ANSHU GAUTAM	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	17	Ok	DRAFT
<input type="checkbox"/>	54	20015567014	20DDUCBSPH000016	ANSHU GAUTAM	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	24	Ok	DRAFT
<input type="checkbox"/>	55	20015567014	20DDUCBSPH000016	ANSHU GAUTAM	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	56	20015567014	20DDUCBSPH000016	ANSHU GAUTAM	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	15	Ok	DRAFT
<input type="checkbox"/>	57	20015567015	20DDUCBSPH000017	ANUJ PRAVESH	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	9	Ok	DRAFT
<input type="checkbox"/>	58	20015567015	20DDUCBSPH000017	ANUJ PRAVESH	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	15	Ok	DRAFT
<input type="checkbox"/>	59	20015567015	20DDUCBSPH000017	ANUJ PRAVESH	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	0	Ok	DRAFT
<input type="checkbox"/>	60	20015567015	20DDUCBSPH000017	ANUJ PRAVESH	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	8	Ok	DRAFT
<input type="checkbox"/>	61	20015567016	20DDUCBSPH000018	DRON JOSHI	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	62	20015567016	20DDUCBSPH000018	DRON JOSHI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	16	Ok	DRAFT
<input type="checkbox"/>	63	20015567016	20DDUCBSPH000018	DRON JOSHI	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	6	Ok	DRAFT
<input type="checkbox"/>	64	20015567016	20DDUCBSPH000018	DRON JOSHI	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	65	20015567017	20DDUCBSPH000019	HARISH	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	17	Ok	DRAFT
<input type="checkbox"/>	66	20015567017	20DDUCBSPH000019	HARISH	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	22	Ok	DRAFT
<input type="checkbox"/>	67	20015567017	20DDUCBSPH000019	HARISH	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	16	Ok	DRAFT
<input type="checkbox"/>	68	20015567017	20DDUCBSPH000019	HARISH	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	17	Ok	DRAFT
<input type="checkbox"/>	69	20015567019	20DDUCBSPH000021	HIMANSHU	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	70	20015567019	20DDUCBSPH000021	HIMANSHU	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	8	Ok	DRAFT
<input type="checkbox"/>	71	20015567019	20DDUCBSPH000021	HIMANSHU	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	0	Ok	DRAFT
<input type="checkbox"/>	72	20015567019	20DDUCBSPH000021	HIMANSHU	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	5	Ok	DRAFT
<input type="checkbox"/>	73	20015567020	20DDUCBSPH000022	HRITIK	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	13	Ok	DRAFT
<input type="checkbox"/>	74	20015567020	20DDUCBSPH000022	HRITIK	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	20	Ok	DRAFT
<input type="checkbox"/>	75	20015567020	20DDUCBSPH000022	HRITIK	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	10	Ok	DRAFT
<input type="checkbox"/>	76	20015567020	20DDUCBSPH000022	HRITIK	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	6	Ok	DRAFT
<input type="checkbox"/>	77	20015567021	20DDUCBSPH000023	KANISHKA VARSHNEY	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	14	Ok	DRAFT

<input type="checkbox"/>	78	20015567021	20DDUCBSPH000023	KANISHKA VARSHNEY	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	23	Ok	DRAFT
<input type="checkbox"/>	79	20015567021	20DDUCBSPH000023	KANISHKA VARSHNEY	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	14	Ok	DRAFT
<input type="checkbox"/>	80	20015567021	20DDUCBSPH000023	KANISHKA VARSHNEY	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	15	Ok	DRAFT
<input type="checkbox"/>	81	20015567022	20DDUCBSPH000024	LAVKUSH PATEL	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	9	Ok	DRAFT
<input type="checkbox"/>	82	20015567022	20DDUCBSPH000024	LAVKUSH PATEL	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	83	20015567022	20DDUCBSPH000024	LAVKUSH PATEL	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	6	Ok	DRAFT
<input type="checkbox"/>	84	20015567022	20DDUCBSPH000024	LAVKUSH PATEL	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	7	Ok	DRAFT
<input type="checkbox"/>	85	20015567023	20DDUCBSPH000025	MANISH KUMAR MINA	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	86	20015567023	20DDUCBSPH000025	MANISH KUMAR MINA	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	10	Ok	DRAFT
<input type="checkbox"/>	87	20015567023	20DDUCBSPH000025	MANISH KUMAR MINA	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	88	20015567023	20DDUCBSPH000025	MANISH KUMAR MINA	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	5	Ok	DRAFT
<input type="checkbox"/>	89	20015567024	ERN20015567024	MAYANK KUMAR	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	90	20015567024	ERN20015567024	MAYANK KUMAR	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	15	Ok	DRAFT
<input type="checkbox"/>	91	20015567024	ERN20015567024	MAYANK KUMAR	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	5	Ok	DRAFT
<input type="checkbox"/>	92	20015567024	ERN20015567024	MAYANK KUMAR	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	5	Ok	DRAFT
<input type="checkbox"/>	93	20015567025	20DDUCBSPH000027	NISHA	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	18	Ok	DRAFT
<input type="checkbox"/>	94	20015567025	20DDUCBSPH000027	NISHA	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	22	Ok	DRAFT
<input type="checkbox"/>	95	20015567025	20DDUCBSPH000027	NISHA	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	16	Ok	DRAFT
<input type="checkbox"/>	96	20015567025	20DDUCBSPH000027	NISHA	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	21	Ok	DRAFT
<input type="checkbox"/>	97	20015567026	20DDUCBSPH000029	NITISH JOON	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	98	20015567026	20DDUCBSPH000029	NITISH JOON	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	13	Ok	DRAFT
<input type="checkbox"/>	99	20015567026	20DDUCBSPH000029	NITISH JOON	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	0	Ok	DRAFT
<input type="checkbox"/>	100	20015567026	20DDUCBSPH000029	NITISH JOON	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	4	Ok	DRAFT
<input type="checkbox"/>	101	20015567027	20DDUCBSPH000030	OMPRAKASH KARWA	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	102	20015567027	20DDUCBSPH000030	OMPRAKASH KARWA	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	8	Ok	DRAFT
<input type="checkbox"/>	103	20015567027	20DDUCBSPH000030	OMPRAKASH KARWA	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	8	Ok	DRAFT
<input type="checkbox"/>	104	20015567027	20DDUCBSPH000030	OMPRAKASH KARWA	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	6	Ok	DRAFT

<input type="checkbox"/>	105	20015567028	20DDUCBSPH000031	PRADEEP YADAV	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	106	20015567028	20DDUCBSPH000031	PRADEEP YADAV	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	17	Ok	DRAFT
<input type="checkbox"/>	107	20015567028	20DDUCBSPH000031	PRADEEP YADAV	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	16	Ok	DRAFT
<input type="checkbox"/>	108	20015567028	20DDUCBSPH000031	PRADEEP YADAV	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	13	Ok	DRAFT
<input type="checkbox"/>	109	20015567029	20DDUCBSPH000032	PRAVEEN KUMAR	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	10	Ok	DRAFT
<input type="checkbox"/>	110	20015567029	20DDUCBSPH000032	PRAVEEN KUMAR	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	14	Ok	DRAFT
<input type="checkbox"/>	111	20015567029	20DDUCBSPH000032	PRAVEEN KUMAR	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	2	Ok	DRAFT
<input type="checkbox"/>	112	20015567029	20DDUCBSPH000032	PRAVEEN KUMAR	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	7	Ok	DRAFT
<input type="checkbox"/>	113	20015567030	20DDUCBSPH000033	PRINCE KUMAR CHOBEY	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	114	20015567030	20DDUCBSPH000033	PRINCE KUMAR CHOBEY	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	19	Ok	DRAFT
<input type="checkbox"/>	115	20015567030	20DDUCBSPH000033	PRINCE KUMAR CHOBEY	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	13	Ok	DRAFT
<input type="checkbox"/>	116	20015567030	20DDUCBSPH000033	PRINCE KUMAR CHOBEY	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	117	20015567031	20DDUCBSPH000034	PRIYANKA	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	16	Ok	DRAFT
<input type="checkbox"/>	118	20015567031	20DDUCBSPH000034	PRIYANKA	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	23	Ok	DRAFT
<input type="checkbox"/>	119	20015567031	20DDUCBSPH000034	PRIYANKA	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	14	Ok	DRAFT
<input type="checkbox"/>	120	20015567031	20DDUCBSPH000034	PRIYANKA	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	20	Ok	DRAFT
<input type="checkbox"/>	121	20015567032	20DDUCBSPH000035	PRIYANSHU KUNWAR	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	15	Ok	DRAFT
<input type="checkbox"/>	122	20015567032	20DDUCBSPH000035	PRIYANSHU KUNWAR	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	24	Ok	DRAFT
<input type="checkbox"/>	123	20015567032	20DDUCBSPH000035	PRIYANSHU KUNWAR	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	124	20015567032	20DDUCBSPH000035	PRIYANSHU KUNWAR	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	14	Ok	DRAFT
<input type="checkbox"/>	125	20015567033	20DDUCBSPH000036	RAHUL	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	126	20015567033	20DDUCBSPH000036	RAHUL	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	13	Ok	DRAFT
<input type="checkbox"/>	127	20015567033	20DDUCBSPH000036	RAHUL	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	128	20015567033	20DDUCBSPH000036	RAHUL	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	129	20015567034	20DDUCBSPH000038	RAHUL BHATTI	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	14	Ok	DRAFT
<input type="checkbox"/>	130	20015567034	20DDUCBSPH000038	RAHUL BHATTI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	18	Ok	DRAFT

<input type="checkbox"/>	131	20015567034	20DDUCBSPH000038	RAHUL BHATTI	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	0	Ok	DRAFT
<input type="checkbox"/>	132	20015567034	20DDUCBSPH000038	RAHUL BHATTI	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	20	Ok	DRAFT
<input type="checkbox"/>	133	20015567035	20DDUCBSPH000037	RAHUL MEENA	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	13	Ok	DRAFT
<input type="checkbox"/>	134	20015567035	20DDUCBSPH000037	RAHUL MEENA	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	21	Ok	DRAFT
<input type="checkbox"/>	135	20015567035	20DDUCBSPH000037	RAHUL MEENA	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	15	Ok	DRAFT
<input type="checkbox"/>	136	20015567035	20DDUCBSPH000037	RAHUL MEENA	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	18	Ok	DRAFT
<input type="checkbox"/>	137	20015567036	20DDUCBSPH000039	RAUNAQ GOHAIN	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	138	20015567036	20DDUCBSPH000039	RAUNAQ GOHAIN	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	18	Ok	DRAFT
<input type="checkbox"/>	139	20015567036	20DDUCBSPH000039	RAUNAQ GOHAIN	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	14	Ok	DRAFT
<input type="checkbox"/>	140	20015567036	20DDUCBSPH000039	RAUNAQ GOHAIN	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	17	Ok	DRAFT
<input type="checkbox"/>	141	20015567037	20DDUCBSPH000040	RHITIK CHAUHAN	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	142	20015567037	20DDUCBSPH000040	RHITIK CHAUHAN	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	8	Ok	DRAFT
<input type="checkbox"/>	143	20015567037	20DDUCBSPH000040	RHITIK CHAUHAN	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	8	Ok	DRAFT
<input type="checkbox"/>	144	20015567037	20DDUCBSPH000040	RHITIK CHAUHAN	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	145	20015567038	20DDUCBSPH000041	RISHIKA THAKUR	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	14	Ok	DRAFT
<input type="checkbox"/>	146	20015567038	20DDUCBSPH000041	RISHIKA THAKUR	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	21	Ok	DRAFT
<input type="checkbox"/>	147	20015567038	20DDUCBSPH000041	RISHIKA THAKUR	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	148	20015567038	20DDUCBSPH000041	RISHIKA THAKUR	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	13	Ok	DRAFT
<input type="checkbox"/>	149	20015567039	20DDUCBSPH000043	ROHAN JOSHI	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	0	Ok	DRAFT
<input type="checkbox"/>	150	20015567039	20DDUCBSPH000043	ROHAN JOSHI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	0	Ok	DRAFT
<input type="checkbox"/>	151	20015567039	20DDUCBSPH000043	ROHAN JOSHI	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	0	Ok	DRAFT
<input type="checkbox"/>	152	20015567039	20DDUCBSPH000043	ROHAN JOSHI	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	0	Ok	DRAFT
<input type="checkbox"/>	153	20015567040	20DDUCBSPH000042	ROHAN SINGH	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	13	Ok	DRAFT
<input type="checkbox"/>	154	20015567040	20DDUCBSPH000042	ROHAN SINGH	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	19	Ok	DRAFT
<input type="checkbox"/>	155	20015567040	20DDUCBSPH000042	ROHAN SINGH	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	156	20015567040	20DDUCBSPH000042	ROHAN SINGH	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	157	20015567041	20DDUCBSPH000044	SACHIN PANDEY	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	11	Ok	DRAFT

<input type="checkbox"/>	158	20015567041	20DDUCBSPH000044	SACHIN PANDEY	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	159	20015567041	20DDUCBSPH000044	SACHIN PANDEY	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	1	Ok	DRAFT
<input type="checkbox"/>	160	20015567041	20DDUCBSPH000044	SACHIN PANDEY	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	8	Ok	DRAFT
<input type="checkbox"/>	161	20015567042	20DDUCBSPH000045	SAKSHI BISHT	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	162	20015567042	20DDUCBSPH000045	SAKSHI BISHT	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	23	Ok	DRAFT
<input type="checkbox"/>	163	20015567042	20DDUCBSPH000045	SAKSHI BISHT	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	14	Ok	DRAFT
<input type="checkbox"/>	164	20015567042	20DDUCBSPH000045	SAKSHI BISHT	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	165	20015567044	20DDUCBSPH000047	SATYAM	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	10	Ok	DRAFT
<input type="checkbox"/>	166	20015567044	20DDUCBSPH000047	SATYAM	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	8	Ok	DRAFT
<input type="checkbox"/>	167	20015567044	20DDUCBSPH000047	SATYAM	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	0	Ok	DRAFT
<input type="checkbox"/>	168	20015567044	20DDUCBSPH000047	SATYAM	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	7	Ok	DRAFT
<input type="checkbox"/>	169	20015567045	20DDUCBSPH000048	SAURAV KUMAR	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	15	Ok	DRAFT
<input type="checkbox"/>	170	20015567045	20DDUCBSPH000048	SAURAV KUMAR	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	18	Ok	DRAFT
<input type="checkbox"/>	171	20015567045	20DDUCBSPH000048	SAURAV KUMAR	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	13	Ok	DRAFT
<input type="checkbox"/>	172	20015567045	20DDUCBSPH000048	SAURAV KUMAR	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	17	Ok	DRAFT
<input type="checkbox"/>	173	20015567047	20DDUCBSPH000050	SHWETA KOTECHA	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	174	20015567047	20DDUCBSPH000050	SHWETA KOTECHA	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	16	Ok	DRAFT
<input type="checkbox"/>	175	20015567047	20DDUCBSPH000050	SHWETA KOTECHA	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	5	Ok	DRAFT
<input type="checkbox"/>	176	20015567047	20DDUCBSPH000050	SHWETA KOTECHA	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	177	20015567048	20DDUCBSPH000051	SHYAM	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	15	Ok	DRAFT
<input type="checkbox"/>	178	20015567048	20DDUCBSPH000051	SHYAM	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	22	Ok	DRAFT
<input type="checkbox"/>	179	20015567048	20DDUCBSPH000051	SHYAM	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	5	Ok	DRAFT
<input type="checkbox"/>	180	20015567048	20DDUCBSPH000051	SHYAM	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	15	Ok	DRAFT
<input type="checkbox"/>	181	20015567049	20DDUCBSPH000052	SIDHARTH	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	182	20015567049	20DDUCBSPH000052	SIDHARTH	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	23	Ok	DRAFT
<input type="checkbox"/>	183	20015567049	20DDUCBSPH000052	SIDHARTH	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	14	Ok	DRAFT
<input type="checkbox"/>	184	20015567049	20DDUCBSPH000052	SIDHARTH	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	12	Ok	DRAFT

<input type="checkbox"/>	185	20015567050	20DDUCBSPH000053	SOMRAJ MEENA	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	186	20015567050	20DDUCBSPH000053	SOMRAJ MEENA	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	10	Ok	DRAFT
<input type="checkbox"/>	187	20015567050	20DDUCBSPH000053	SOMRAJ MEENA	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	188	20015567050	20DDUCBSPH000053	SOMRAJ MEENA	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	5	Ok	DRAFT
<input type="checkbox"/>	189	20015567051	20DDUCBSPH000054	SONU KUMAR	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	10	Ok	DRAFT
<input type="checkbox"/>	190	20015567051	20DDUCBSPH000054	SONU KUMAR	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	9	Ok	DRAFT
<input type="checkbox"/>	191	20015567051	20DDUCBSPH000054	SONU KUMAR	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	192	20015567051	20DDUCBSPH000054	SONU KUMAR	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	10	Ok	DRAFT
<input type="checkbox"/>	193	20015567052	20DDUCBSPH000055	SPARSH JAIN	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	15	Ok	DRAFT
	194	20015567052	20DDUCBSPH000055	SPARSH JAIN	32227627	DISSERTATION	6 SEMESTER			Not Entered	
<input type="checkbox"/>	195	20015567052	20DDUCBSPH000055	SPARSH JAIN	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	1	Ok	DRAFT
<input type="checkbox"/>	196	20015567052	20DDUCBSPH000055	SPARSH JAIN	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	13	Ok	DRAFT
<input type="checkbox"/>	197	20015567053	20DDUCBSPH000056	TANISHA	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	13	Ok	DRAFT
<input type="checkbox"/>	198	20015567053	20DDUCBSPH000056	TANISHA	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	21	Ok	DRAFT
<input type="checkbox"/>	199	20015567053	20DDUCBSPH000056	TANISHA	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	15	Ok	DRAFT
<input type="checkbox"/>	200	20015567053	20DDUCBSPH000056	TANISHA	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	15	Ok	DRAFT
<input type="checkbox"/>	201	20015567054	20DDUCBSPH000057	TUSHAR	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	9	Ok	DRAFT
<input type="checkbox"/>	202	20015567054	20DDUCBSPH000057	TUSHAR	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	9	Ok	DRAFT
<input type="checkbox"/>	203	20015567054	20DDUCBSPH000057	TUSHAR	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	0	Ok	DRAFT
<input type="checkbox"/>	204	20015567054	20DDUCBSPH000057	TUSHAR	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	6	Ok	DRAFT
<input type="checkbox"/>	205	20015567055	20DDUCBSPH000058	UPASANA GAHTORI	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	15	Ok	DRAFT
<input type="checkbox"/>	206	20015567055	20DDUCBSPH000058	UPASANA GAHTORI	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	10	Ok	DRAFT
<input type="checkbox"/>	207	20015567055	20DDUCBSPH000058	UPASANA GAHTORI	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	12	Ok	DRAFT
<input type="checkbox"/>	208	20015567055	20DDUCBSPH000058	UPASANA GAHTORI	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	13	Ok	DRAFT
<input type="checkbox"/>	209	20015567056	20DDUCBSPH000059	VIDHAN CHANDEL	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	10	Ok	DRAFT
<input type="checkbox"/>	210	20015567056	20DDUCBSPH000059	VIDHAN CHANDEL	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	3	Ok	DRAFT
<input type="checkbox"/>	211	20015567056	20DDUCBSPH000059	VIDHAN CHANDEL	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	2	Ok	DRAFT



<input type="checkbox"/>	212	20015567056	20DDUCBSPH000059	VIDHAN CHANDEL	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	6	Ok	DRAFT
<input type="checkbox"/>	213	20015567058	20DDUCBSPH000060	VIKAS KUMAR	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	14	Ok	DRAFT
<input type="checkbox"/>	214	20015567058	20DDUCBSPH000060	VIKAS KUMAR	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	16	Ok	DRAFT
<input type="checkbox"/>	215	20015567058	20DDUCBSPH000060	VIKAS KUMAR	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	10	Ok	DRAFT
<input type="checkbox"/>	216	20015567058	20DDUCBSPH000060	VIKAS KUMAR	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	15	Ok	DRAFT
<input type="checkbox"/>	217	20015567059	20DDUCBSPH000062	YASH	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	9	Ok	DRAFT
<input type="checkbox"/>	218	20015567059	20DDUCBSPH000062	YASH	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	10	Ok	DRAFT
<input type="checkbox"/>	219	20015567059	20DDUCBSPH000062	YASH	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	220	20015567059	20DDUCBSPH000062	YASH	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	221	20015567060	20DDUCBSPH000014	ANKIT RANA	32227625	ADVANCED MATHEMATICAL PHYSICS-II	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	222	20015567060	20DDUCBSPH000014	ANKIT RANA	32227628	VERILOG AND FPGA BASED SYSTEM DESIGN	6 SEMESTER	25.00	16	Ok	DRAFT
<input type="checkbox"/>	223	20015567060	20DDUCBSPH000014	ANKIT RANA	32221601	ELECTROMAGNETIC THEORY	6 SEMESTER	25.00	11	Ok	DRAFT
<input type="checkbox"/>	224	20015567060	20DDUCBSPH000014	ANKIT RANA	32221602	STATISTICAL MECHANICS	6 SEMESTER	25.00	9	Ok	DRAFT