

## DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

**B. Sc. Hons Electronics I - Sem**

**w.e.f August 23, 2017 (revised)**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>	Generic 104	Generic 104	Net. Anal. 104 E-1	Net. Anal. 104 E-1	<b>LUNCH BREAK</b>				
<b>Tuesday</b>	Math. Fnd 104 E-2	Math. Fnd 104 E-2				Net. Anal. 104 E-1	Net. Anal. 104 E-1	HINDI 104	HINDI 104
<b>Wednesday</b>	Math. Fnd 104 E-2	Math. Fnd 104 E-2	AECC 104 JK	AECC 104 JK		Generic Lab/Tutorial			
<b>Thursday</b>	Network Analysis Lab. E-1, E-3, RK Electronics Lab. 116					Generic 104	Generic 104	HINDI 104	HINDI 104
<b>Friday</b>			AECC 104 ET1	AECC 104 ET1		Mathematical Foundation Lab. E-2, AM, NT Electronics Lab. 116 and 117			
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

No Change in the Time Table is possible without the permission of the Principal

RK	Dr. Ravinder Kaur	E-1	Mr. Ajit Singh
PKS	Dr. Poonam Kasturi	E-2	Ms. Neha
NT	Dr. Neeraj Tyagi	E-3	Mr. Naveen Kr.
AM	Dr. Anurag Mishra	ET1	English Teacher
MS	Dr. Manoj Saxena		
JK	Dr. Jyotishman Kalita		

Convener Time Table

Principal

## DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for: **B. Sc. Hons Electronics III - Sem** **w.e.f August 23, 2017 (revised)**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>	Digital Electronics and Verilog Lab. PKS, MS, E-2 Electronics Lab. 116 and 117				<b>LUNCH BREAK</b>	Electronic Cir. 111 MS	Electronic Cir. 111 MS	C Prog. 111 AM	C Prog. 111 AM
<b>Tuesday</b>	Electronic Cir. 115 MS	Electronic Cir. 115 MS	Digital Elec. 115 PKS	Digital Elec. 115 PKS		Generic Lab./Tutorial			
<b>Wednesday</b>	Generic 115	Generic 115	Digital Elec. 115 PKS	Digital Elec. 115 PKS		C Programming and Data Structures Lab. AM,RK, E-1 and E-3 Electronics Lab. 117			
<b>Thursday</b>	Electronic Circuits Lab. PKS, MS, E-2 Electronics Lab. 117					C Prog. 115 AM	C Prog. 115 AM	SEC 115 E-2	SEC 115 E-2
<b>Friday</b>	Skill Enhancement Course Lab. RK, E-1 and E-2 Electronics Lab. 117					Generic 115	Generic 115		
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

No Change in the Time Table is possible without the permission of the Principal

RK	Dr. Ravinder Kaur	E-1	Mr. Ajit Singh
PKS	Dr. Poonam Kasturi	E-2	Ms. Neha
NT	Dr. Neeraj Tyagi	E-3	Mr. Naveen Kr.
AM	Dr. Anurag Mishra		
MS	Dr. Manoj Saxena		

Convener Time Table

Principal

## DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

**B. Sc. Hons Electronics V - Sem**

**w.e.f August 23, 2017 (revised)**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm	
<b>Monday</b>	Semi. Fab. 115 NT	Semi. Fab. 115 NT	Microproc. 115 RK	Microproc. 115 RK	<b>LUNCH BREAK</b>	DSP 115 E-3	DSP 115 E-3			
<b>Tuesday</b>			Microproc. 104 RK	Microproc. 104 RK		Microprocessors and Microcontroller Lab. RK, AM,NT, E-3 Electronics Lab. 117				
<b>Wednesday</b>	Electromagnetics Lab. E-1,NT, MS Electronics Lab. 116 and 117						EMT 115 E-1	EMT 115 E-1	DSP 115 E-3	DSP 115 E-3
<b>Thursday</b>			Semi. Fab. 115 NT	Semi. Fab. 115 NT		Semiconductor Fabrication and Charecterization Lab. NT, E-2 and E-3 Electronics Lab. 116 and 117				
<b>Friday</b>	DSP Lab. E-3, PKS, MS Electronics Lab. 116						EMT 104 E-1	EMT 104 E-1		
<b>Saturday</b>										

Classes begin at 9:00 am on all days.

No Change in the Time Table is possible without the permission of the Principal

RK	Dr. Ravinder Kaur	E-1	Mr. Ajit Singh
PKS	Dr. Poonam Kasturi	E-2	Ms. Neha
NT	Dr. Neeraj Tyagi	E-3	Mr. Naveen Kr.
AM	Dr. Anurag Mishra		
MS	Dr. Manoj Saxena		

Convener Time Table

Principal

## DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

**Dr. Ravinder Kaur**

**w.e.f August 23, 2017 (revised)**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>			Microproc. 115 EH-V	Microproc. 115 EH-V	<b>LUNCH BREAK</b>				
<b>Tuesday</b>			Microproc. 104 EH-V	Microproc. 104 EH-V		Microprocessors and Microcontroller Lab. EH-V Sem Electronics Lab. 117			
<b>Wednesday</b>						C Prog. and DS Lab. EH-III Sem Electronics Lab. 117			
<b>Thursday</b>			Network Analysis Lab. EH-I Sem Electronics Lab. 116						
<b>Friday</b>	Skill Enhancement Course Lab. EH-III Sem Electronics Lab. 117								
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

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Convener Time Table

Principal

## DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

**Dr. Poonam Kasturi**

**w.e.f August 23, 2017 (revised)**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm		5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>	Digital Electronics and Verilog Lab. EH-III Sem Electronics Lab. 116 and 117				<b>LUNCH BREAK</b>				
<b>Tuesday</b>			Digital Elec. 115 EH-III Sem	Digital Elec. 115 EH-III Sem					
<b>Wednesday</b>			Digital Elec. 115 EH-III Sem	Digital Elec. 115 EH-III Sem					
<b>Thursday</b>	Electronic Circuits Lab. EH-III Sem Electronics Lab. 117								
<b>Friday</b>	DSP Lab. EH-V Sem Electronics Lab. 116								
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

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Convener Time Table

Principal

## DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

**Dr. Anurag Mishra**

**w.e.f August 23, 2017 (revised)**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>					<b>LUNCH BREAK</b>			C Prog. 111 EH-III Sem	C Prog. 111 EH-III Sem
<b>Tuesday</b>						Microprocessors and Microcontroller Lab. EH-V Sem Electronics Lab. 117			
<b>Wednesday</b>						C Programming and Data Structures Lab. EH-III Sem Electronics Lab. 117			
<b>Thursday</b>						C Prog. 115 EH-III Sem	C Prog. 115 EH-III Sem		
<b>Friday</b>						Mathematical Foundation Lab. EH-I Sem Electronics Lab. 116 and 117			
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

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Convener Time Table

Principal

## DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

**Dr. Neeraj Tyagi**

**w.e.f August 23, 2017 (revised)**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm	
<b>Monday</b>	Semi. Fab. 115 EH-V Sem	Semi. Fab. 115 EH-V Sem			<b>LUNCH BREAK</b>					
<b>Tuesday</b>							Microprocessors Lab. EH-V Sem Electronics Lab. 117			
<b>Wednesday</b>	Electromagnetics Lab. EH-V Sem Electronics Lab. 116 and 117									
<b>Thursday</b>			Semi. Fab. 115 EH-V Sem	Semi. Fab. 115 EH-V Sem			Semiconductor Fab.Charecterization Lab. EH-V Sem Electronics Lab. 116 and 117			
<b>Friday</b>							Mathematical Foundation Lab. EH-I Sem Electronics Lab. 116 and 117			
<b>Saturday</b>										

Classes begin at 9:00 am on all days.

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Convener Time Table

Principal

## DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

**Dr. Manoj Saxena**

**w.e.f August 23, 2017 (revised)**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm		5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>	Digital Electronics and Verilog Lab. EH-III Sem Electronics Lab. 116 and 117				<b>LUNCH BREAK</b>	Electronic Cir. 111 EH-III Sem	Electronic Cir. 111 EH-III Sem		
<b>Tuesday</b>	Electronic Cir. 115 EH-III Sem	Electronic Cir. 115 EH-III Sem							
<b>Wednesday</b>	Electromagnetics Lab. EH-V Sem Electronics Lab. 116 and 117								
<b>Thursday</b>	Electronic Circuits Lab. EH-III Sem Electronics Lab. 117								
<b>Friday</b>	DSP Lab. EH-V Sem Electronics Lab. 116								
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

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Convener Time Table

Principal



## DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

**E-1**

**Mr. Ajit Singh**

**w.e.f August 23, 2017 (revised)**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm	
<b>Monday</b>			Net. Anal. 104 EH-I Sem	Net. Anal. 104 EH-I Sem	<b>LUNCH BREAK</b>					
<b>Tuesday</b>						Net. Anal. 104 EH-I Sem	Net. Anal. 104 EH-I Sem			
<b>Wednesday</b>			Electromagnetics Lab. EH-V Sem Electronics Lab. 116 and 117			EMT 115 EH-V Sem	EMT 115 EH-V Sem	C Prog. and DS Lab. EH-III Sem Electronics Lab. 117		
<b>Thursday</b>			Network Analysis Lab. EH-I Sem Electronics Lab. 116							
<b>Friday</b>	Skill Enhan. Course Lab. EH-III Sem Electronics Lab. 117					EMT 104 EH-V Sem	EMT 104 EH-V Sem			
<b>Saturday</b>										

Classes begin at 9:00 am on all days.

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Convener Time Table

Principal

## DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

E-2

Ms. Neha

w.e.f August 23, 2017 (revised)

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm	
<b>Monday</b>	Digital Elect. & Verilog Lab. EH-III Sem Electronics Lab. 116 and 117				<b>LUNCH BREAK</b>					
<b>Tuesday</b>	Math. Fnd 104 EH-I Sem	Math. Fnd 104 EH-I Sem								
<b>Wednesday</b>	Math. Fnd 104 EH-I Sem	Math. Fnd 104 EH-I Sem								
<b>Thursday</b>			Electronic Circuits Lab. EH-III Sem Electronics Lab. 117				Semiconductor Fab.Lab. EH-V Sem Electronics Lab. 116 and 117		SEC 115 EH-III Sem	SEC 115 EH-III Sem
<b>Friday</b>		Skill Enhan. Course Lab. EH-III Sem Electronics Lab. 117						Mathematical Foundation Lab. EH-I Sem Electronics Lab. 116 and 117		
<b>Saturday</b>										

Classes begin at 9:00 am on all days.

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Convener Time Table

Principal

## DEEN DAYAL UPADHYAYA COLLEGE (UNIVERSITY OF DELHI)

Time table for:

**E-3**

**Mr. Naveen Kr.**

**w.e.f August 23, 2017 (revised)**

	1 9 am-10 am	2 10 am-11 am	3 11 am-12 pm	4 12 pm-1 pm	1 pm-1:30 pm	5 1:30 pm-2:30 pm	6 2:30pm -3:30 pm	7 3:30 pm-4:30 pm	8 4:30 pm-5:30 pm
<b>Monday</b>					<b>LUNCH BREAK</b>	DSP 115 EH-V Sem	DSP 115 EH-V Sem		
<b>Tuesday</b>								Microprocessors Lab. EH-V Sem Electronics Lab. 117	
<b>Wednesday</b>							C Prog. and DS Lab. EH-III Sem Electronics Lab. 117	DSP 115 EH-V Sem	DSP 115 EH-V Sem
<b>Thursday</b>	Network Analysis Lab. EH-I Sem Electronics Lab. 116							Semiconductor Fab. Lab. EH-V Sem Electronics Lab. 116 and 117	
<b>Friday</b>	DSP Lab. EH-V Sem Electronics Lab. 116								
<b>Saturday</b>									

Classes begin at 9:00 am on all days.

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Convener Time Table

Principal